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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		
	Filing Date		
	First Named Inventor	Przybylek Piotr	
	Art Unit		
	Examiner Name		
	Attorney Docket Number	LHUD-02501-NUS	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5651128		07/22/1997	Gaultier, Jean-Marie	

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	1	0991081	EP		04/05/2000	Lardeau, Celine		<input type="checkbox"/>

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	1	BAHOUT Y: "Combined FLASH and EEPROM integrated circuit", ELECTRONIK INDUSTRIE, XX, XX, vol.28, no. 10, 1997, pages 48, 50-51	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number	
Filing Date	
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Examiner Name	
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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☐ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Matthias Scholl/	Date (YYYY-MM-DD)	2006-05-15
Name/Print	Dr. Matthias SCHOLL	Registration Number	54947

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